

What is claimed is:

1. A circuit for generating dot clock pulses utilized for an image-forming apparatus having an image-writing section, comprising:

a digital-delay dot clock adjusting section to generate first dot clock pulses having a predetermined number of pulses within a predetermined time interval at a constant exposing range of said image-writing section, wherein each period of said first dot clock pulses is slightly increased or reduced by changing a selection for a plurality of delayed clock pulses, which are generated by delaying clock-pulses, outputted from a reference oscillator, in slightly different delay times; and

a jitter suppressing section to suppress a jitter component included in said first dot clock pulses, wherein said jitter suppressing section divides said first dot clock pulses to generate second dot clock pulses, and then, multiplies said second dot clock pulses to generate said dot clock pulses.

2. The circuit of claim 1,

wherein said jitter suppressing section comprises,

a voltage controlled oscillator to generate said second dot clock pulses;

a first divider to divide said first dot clock pulses outputted by said digital-delay dot clock adjusting section;

a second divider to divide said second dot clock pulses generated by said voltage controlled oscillator; and

a phase comparator to perform a frequency-phase comparison between first divided dot clock pulses outputted by said first divider and second divided dot clock pulses outputted by said second divider to output a comparison result voltage, and

wherein said phase comparator feedbacks said comparison result voltage to said voltage controlled oscillator so as to constitute a phase locked loop, serving as a flywheel oscillator to disperse said jitter component.

3. An image-forming apparatus, comprising:

an image-bearing member to bear a electrostatic latent image and/or a toner image on it;

an image-writing device to scan a surface of said image-bearing member with a scanning-light deflected by a rotating polygon mirror;

a modulating section that performs a pulse width modulation or a light-intensity modulation of dot clock pulses in response to image data to generate a scanning-light modulation signal to be fed to said image-writing device;

a developing section that develops said electrostatic latent image, formed on said image-bearing member by said scanning-light, to form said toner image as a visual image;

a transferring section to transfer said toner image borne on said image-bearing member to a transfer material;

a fixing section to fix said toner image, transferred to said transfer material, onto said transfer material;

a digital-delay dot clock adjusting section to generate first dot clock pulses having a predetermined number of pulses within a predetermined time interval at a constant exposing range of said image-writing section, wherein each period of said first dot clock pulses is slightly increased or reduced by changing a selection for a plurality of delayed clock pulses, which are generated by delaying clock-pulses, outputted from a reference oscillator, in slightly different delay times; and

a jitter suppressing section to suppress a jitter component included in said first dot clock pulses, wherein said jitter suppressing section divides said first dot clock

pulses to generate second dot clock pulses, and then, multiplies said second dot clock pulses to generate said dot clock pulses.

4. The image-forming apparatus of claim 3,

wherein said jitter suppressing section comprises,

a voltage controlled oscillator to generate said second dot clock pulses;

a first divider to divide said first dot clock pulses outputted by said digital-delay dot clock adjusting section;

a second divider to divide said second dot clock pulses generated by said voltage controlled oscillator; and

a phase comparator to perform a frequency-phase comparison between first divided dot clock pulses outputted by said first divider and second divided dot clock pulses outputted by said second divider to output a comparison result voltage, and

wherein said phase comparator feedbacks said comparison result voltage to said voltage controlled oscillator so as to constitute a phase locked loop, serving as a flywheel oscillator to disperse said jitter component.

5. The image-forming apparatus of claim 3,

wherein said image-forming apparatus forms a color image based on a plurality of primary colors, and said image-writing device, said digital-delay dot clock adjusting section and said jitter suppressing section are provided corresponding to each of said primary colors.

6. The image-forming apparatus of claim 3,

wherein said image-forming apparatus forms an obverse image and a reverse image on both sides of said transfer material, so that a position of said obverse image coincides with a position of said reverse image.